

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)													
Choice Based Credit System (CBCS) Scheme													
Bachelor of Technology (Electronics & Communication)													
VII - SEMESTER(w.e.f. Batch 2018-19)													
Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Asses- ment*	End Sem University Exam	Teachers Asses- ment*		
1	BTEC714	Embedded Systems for Robotics		3	1	2	60	20	20	30	20	150	5
2	BTEC702	Advanced Communication Systems		3	0	2	60	20	20	30	20	150	4
3	BTEC713/ BTEC701/ BTECIOT711	Electives: 1 Wireless Networks 2 Optical and Satellite Communication 3 Principles of Artificial Intelligence and Machine Learning		3	0	0	60	20	20	0	0	100	3
4	BTEC723/ BTECIOT601/ BTEI601	Electives: 1 Advanced Microcontroller and Embedded Systems 2 Application Development for IOT 3 Biomedical Instrumentation		3	0	2	60	20	20	30	20	150	4
5	BBAI501	Human Values and Professional Ethics		4	0	0	60	20	20	0	0	100	4
6	BTEC705	Major Project		0	0	8	0	0	0	90	60	150	4
TOTAL				16	1	14	300	100	100	180	120	800	24
*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.													
* Students are advised to take online MOOCS course of minimum two credits													



Chairperson
Board of Studies

Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore



Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, INDORE



Controller of Examinations
Shri Vaishnav Vidyapeeth Vishwavidyalaya



Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore



Vice Chancellor
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore